Class Schedule by State

Important Notice:

1. Class Schedule list below is as of January 05, 2009.
2. Class dates are subject to change due to low enrollment. Please contact your local training representative if you have any questions.
3. After clicking on a list to register for a class offered by Xilinx, you will be required to login to our training catalog to checkout. If you have already created your account, please login with your username and password. If you have not created your account, please click the link "New User?" then proceed

If you have any questions, please contact the Registrar at (877) XLX-CLASS or registrar@xilinx.com

AL

No classes scheduled for the next 4 months in this location. Request a Private or Public Class

ALBERTA

For this location, please click here to visit our ATP site for the complete class schedule

AZ

Advanced FPGA Implementation

Advanced FPGA Implementation tackles the most sophisticated aspects of the ISE® 10.1 design suite and Xilinx hardware. Seven labs provide hands-on experience in this two-day course and cover the Xilinx Synthesis Technology (XST) tools. This course requires the Fundamentals of FPGA Design and Designing for Performance courses as prerequisites. An intermediate knowledge of Verilog or VHDL is strongly recommended as is at least six months of design experience with Xilinx tools and FPGAs. The lecture material in this course covers the ISE 10.1 tools and the Virtex®-5 and Spartan®-3E FPGAs. For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
1/15/09 - 1/16/09	00057767	AZ	Black Canyon Conference Center/Phoenix	1000	10	Click here to Login & Register

Designing for Performance

Attending the Designing for Performance class will help you create more efficient designs. This course can help you fit your design into a smaller FPGA or a lower speed grade for reducing system costs. In addition, by mastering the tools and the design methodologies presented in this course, you will be able to create your design faster, shorten your development time, and lower development costs. For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
2/24/09 - 2/25/09	00057789	AZ	Black Canyon Conference Center/Phoenix	1000	10	Click here to Login & Register

Designing with the PlanAhead Analysis and Design Tool

Learn to increase design performance and achieve repeatable results by using the PlanAhead software tool. Topics include: a product overview, synthesis and project tips, design analysis, creating a floorplan, improving performance, experimenting with implementation options, incremental methodology, block-based IP design, and I/O pin assignment.

Note: The hands-on labs provided within this course are identical to the tutorials that are packaged with the PlanAhead tool. This course is supplemented with instructor-led presentations and demos. For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
2/26/09 - 2/27/09	00057790	AZ	Black Canyon Conference Center/Phoenix	1000	10	Click here to Login & Register

Fundamentals of FPGA Design

Use the ISE® software tools to implement a design and gain a firm understanding of the Xilinx FPGA architecture. Lea the best design practices from the pros and understand the subtleties of the Xilinx design flow. This course covers ISE 10.1 features, such as the Architecture Wizard and the Floorplan Editor. Other topics include design planning, implementation options, and global timing constraints. For more emphasis on improving the overall design performanc take the follow-up course Designing for Performance, which builds on the basic principles covered in this course. Note that one of the prerequisites of Fundamentals of FPGA Design is the completion of the basic FPGA architecture modules. For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
2/23/09 - 2/23/09	00057788	AZ	Black Canyon Conference Center/Phoenix	500	5	Click here to Login & Register

For this location, please click here to visit our ATP site for the complete class schedule

BRITISH COLUMBIA

No classes scheduled for the next 4 months in this location. Request a Private or Public Class

CA - NORTHERN

Advanced Features and Techniques of Embedded Systems Development

Advanced Features and Techniques of Embedded Systems Development provides embedded systems developers the necessary skills to develop complex embedded systems and enables them to improve their designs by using the tools available in the Embedded Development Kit (EDK). This course also helps developers understand and utilize advance components of embedded systems design for architecting a complex system. For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
2/4/09 - 2/5/09	00058228	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1200	12	Click here to Login & Register
4/7/09 - 4/8/09	00057163	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1200	12	Click here to Login & Register

Advanced FPGA Implementation

Advanced FPGA Implementation tackles the most sophisticated aspects of the ISE® 10.1 design suite and Xilinx hardware. Seven labs provide hands-on experience in this two-day course and cover the Xilinx Synthesis Technology (XST) tools. This course requires the Fundamentals of FPGA Design and Designing for Performance courses as prerequisites. An intermediate knowledge of Verilog or VHDL is strongly recommended as is at least six months of design experience with Xilinx tools and FPGAs. The lecture material in this course covers the ISE 10.1 tools and the Virtex®-5 and Spartan®-3E FPGAs. For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
1/29/09 - 1/30/09	00057223	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1000	10	Click here to Login & Register
3/24/09 - 3/25/09	00058567	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1000	10	Click here to Login & Register
4/7/09 - 4/8/09	00058607	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1000	10	Click here to Login & Register

Designing a LogiCORE PCI Express System

Attending the Designing a LogiCORE PCI Express System will provide you a working knowledge of how to implement Xilinx PCI Express® core in your applications. This course focuses on the implementation of a Xilinx PCI Express system with supporting logic and example designs. With this experience, you can improve your time to market with you PCIe core design. Various Xilinx PCI Express core products will be enumerated to aid you in selecting the proper solution. This course focuses on the Virtex®-5 FPGA PCIe Endpoint Block Plus and the Spartan®-3 PCIe integrated Endpoint PIPE block. For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
3/3/09 - 3/4/09	00058232	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1000	10	Click here to Login & Register

Designing for Performance

Attending the Designing for Performance class will help you create more efficient designs. This course can help you fit your design into a smaller FPGA or a lower speed grade for reducing system costs. In addition, by mastering the tools and the design methodologies presented in this course, you will be able to create your design faster, shorten your development time, and lower development costs. For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
1/21/09 - 1/22/09	00057169	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1000	10	Click here to Login & Register
2/19/09 - 2/20/09	00055888	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1000	10	Click here to Login & Register
3/11/09 - 3/12/09	00058235	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1000	10	Click here to Login & Register
4/2/09 - 4/3/09	00058606	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1000	10	Click here to Login & Register

Designing with the PlanAhead Analysis and Design Tool

Learn to increase design performance and achieve repeatable results by using the PlanAhead software tool. Topics include: a product overview, synthesis and project tips, design analysis, creating a floorplan, improving performance, experimenting with implementation options, incremental methodology, block-based IP design, and I/O pin assignment.

Note: The hands-on labs provided within this course are identical to the tutorials that are packaged with the PlanAhead tool. This course is supplemented with instructor-led presentations and demos. For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
3/5/09 - 3/6/09	00058233	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1000	10	Click here to Login & Register

DSP Design Using System Generator

This course allows you to explore the System Generator tool and to gain the expertise you need to develop advanced, low-cost DSP designs. This intermediate course in implementing DSP functions focuses on learning how to use System Generator for DSP, design implementation tools, and hardware co-simulation verification. Through hands-on exercises you will implement a design from algorithm concept to hardware verification using the Xilinx FPGA capabilities. For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
3/19/09 - 3/20/09	00058566	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1200	12	Click here to Login & Register

DSP Implementation Techniques for Xilinx FPGAs (v8)

This course illustrates how to take advantage of the features available in the Xilinx FPGA architecture and describes how DSP algorithms can be efficiently implemented. The techniques also demonstrate which decisions at the system level have the greatest impact on the implementation process and product costs.

Date	Class#	Location	Facility	Price (USD)	TC	
2/18/09 - 2/20/09	00058231	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1800	18	Click here to Login & Register
4/1/09 - 4/3/09	00058568	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1800	18	Click here to Login & Register
4/28/09 - 4/30/09	00058626	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1800	18	Click here to Login & Register

Embedded Open-Source Linux Development

This intermediate-level, two-day course provides embedded systems developers with experience in creating an embedded open-source Linux operating system on a Xilinx development board. The course offers students hands-on experience from building the environment to booting the system using a basic, single-processor System on Chip (SoC) design with Linux 2.6 from the Xilinx kernel tree. This course introduces embedded Linux components, use of open-source components, environment configurations, network components, and debugging/profiling options for embedded Linux platforms. The primary focus is on embedded Linux development in conjunction with the Xilinx tool flo For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
3/17/09 - 3/18/09	00058565	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1200	12	Click here to Login & Register

Embedded Systems Development

Xilinx FPGAs provide a new level of system design capabilities through soft MicroBlaze™ processors, hard PowerPC® processors, and silicon-efficient architectural resources. This course brings experienced FPGA designers up to speed on developing embedded systems using the Embedded Development Kit (EDK). The features and capabilities of the Xilinx MicroBlaze soft processor and the PowerPC 440 processor are also included in the lectures and labs. The hands-on labs provide experience with the development, debugging, and simulation of an embedded system. For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
3/3/09 - 3/4/09	00054763	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1200	12	Click here to Login & Register

Embedded Systems Software Development

This two-day course introduces you to software design and development for Xilinx embedded processor systems. You will learn the basic tool use and concepts required for the software phase of the design cycle, after the hardware design is completed.

While this course includes many of the topics presented in the Embedded Systems Development and Advanced Features and Techniques of Embedded Systems Development courses, the focus is on software development concepts and practices rather than hardware development. Hardware design concepts and procedures are not covered.

For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
2/24/09 - 2/25/09	00057524	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1200	12	Click here to Login & Register

Fundamentals of FPGA Design

Use the ISE® software tools to implement a design and gain a firm understanding of the Xilinx FPGA architecture. Learn the best design practices from the pros and understand the subtleties of the Xilinx design flow. This course covers ISE 10.1 features, such as the Architecture Wizard and the Floorplan Editor. Other topics include design planning, implementation options, and global timing constraints. For more emphasis on improving the overall design performance take the follow-up course Designing for Performance, which builds on the basic principles covered in this course. Note that one of the prerequisites of Fundamentals of FPGA Design is the completion of the basic FPGA architecture modules. For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
1/20/09 - 1/20/09	00057167	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	500	5	Click here to Login & Register
2/18/09 - 2/18/09	00055887	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	500	5	Click here to Login & Register
3/10/09 - 3/10/09	00058234	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	500	5	Click here to Login & Register
4/1/09 - 4/1/09	00058605	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	500	5	Click here to Login & Register

Introduction to Verilog

This course is a thorough introduction to the Verilog language. It emphasizes writing Register Transfer Level (RTL) and behavioral source code and targets Xilinx devices specifically and FPGA devices in general. This course also combines insightful lectures with practical lab exercises to reinforce key concepts. You will learn advanced coding techniques that will increase your overall Verilog proficiency and enhance your FPGA optimization.

Date	Class#	Location	Facility	Price (USD)	TC	
1/26/09 - 1/28/09	00057170	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	1500	15	Click here to Login & Register

Minimizing Your Design Time with the ChipScope Pro Debug and Verification Tools

Date	Class#	Location	Facility	Price (USD)	TC	
2/3/09 - 2/3/09	00054743	CA - NORTHERN	Xilinx Learning Center - San Jose, CA, USA	500	5	Click here to Login & Register

CA - SOUTHERN

Advanced Features and Techniques of Embedded Systems Development

Advanced Features and Techniques of Embedded Systems Development provides embedded systems developers the necessary skills to develop complex embedded systems and enables them to improve their designs by using the tools available in the Embedded Development Kit (EDK). This course also helps developers understand and utilize advance components of embedded systems design for architecting a complex system. For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
2/11/09 - 2/12/09	00057787	CA - SOUTHERN	Holiday Inn - Costa Mesa Orange Co. Airport	1200	12	Click here to Login & Register

Designing for Performance

Attending the Designing for Performance class will help you create more efficient designs. This course can help you fit your design into a smaller FPGA or a lower speed grade for reducing system costs. In addition, by mastering the tools and the design methodologies presented in this course, you will be able to create your design faster, shorten your development time, and lower development costs. For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
1/27/09 - 1/28/09	00057770	CA - SOUTHERN	Holiday Inn - Costa Mesa Orange Co. Airport	1000	10	Click here to Login & Register

Designing with the PlanAhead Analysis and Design Tool

Learn to increase design performance and achieve repeatable results by using the PlanAhead software tool. Topics include: a product overview, synthesis and project tips, design analysis, creating a floorplan, improving performance, experimenting with implementation options, incremental methodology, block-based IP design, and I/O pin assignment.

Note: The hands-on labs provided within this course are identical to the tutorials that are packaged with the PlanAhead tool. This course is supplemented with instructor-led presentations and demos. For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
1/29/09 - 1/30/09	00057771	CA - SOUTHERN	Holiday Inn - Costa Mesa Orange Co. Airport	1000	10	Click here to Login & Register

Embedded Systems Development

Xilinx FPGAs provide a new level of system design capabilities through soft MicroBlaze™ processors, hard PowerPC® processors, and silicon-efficient architectural resources. This course brings experienced FPGA designers up to speed on developing embedded systems using the Embedded Development Kit (EDK). The features and capabilities of the Xilinx MicroBlaze soft processor and the PowerPC 440 processor are also included in the lectures and labs. The hands-on labs provide experience with the development, debugging, and simulation of an embedded system. For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
2/9/09 - 2/10/09	00057786	CA - SOUTHERN	Holiday Inn - Costa Mesa Orange Co. Airport	1200	12	Click here to Login & Register

Fundamentals of FPGA Design

Use the ISE® software tools to implement a design and gain a firm understanding of the Xilinx FPGA architecture. Lea the best design practices from the pros and understand the subtleties of the Xilinx design flow. This course covers ISE 10.1 features, such as the Architecture Wizard and the Floorplan Editor. Other topics include design planning, implementation options, and global timing constraints. For more emphasis on improving the overall design performanc take the follow-up course Designing for Performance, which builds on the basic principles covered in this course. Note that one of the prerequisites of Fundamentals of FPGA Design is the completion of the basic FPGA architecture modules. For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
1/26/09 - 1/26/09	00057769	CA - SOUTHERN	Holiday Inn - Costa Mesa Orange Co. Airport	500	5	Click here to Login & Register

Introduction to VHDL

This comprehensive course is a thorough introduction to the VHDL language. The emphasis is on writing Register Transfer Level (RTL) and behavioral source code. This class addresses targeting Xilinx devices specifically and FPGA devices in general. The information gained can be applied to any digital design by using a top-down synthesis design approach. You will also learn advanced coding techniques that will increase your overall VHDL proficiency and prepar you for the Advanced VHDL course.

Date	Class#	Location	Facility	Price (USD)	TC	
1/19/09 - 1/21/09	00057768	CA - SOUTHERN	Holiday Inn - Costa Mesa Orange Co. Airport	1500	15	Click here to Login & Register
2/2/09 - 2/4/09	00057785	CA - SOUTHERN	Embassy Suites LAX North	1500	15	Click here to Login & Register

For this location, please click here to visit our ATP site for the complete class schedule

CO

No classes scheduled for the next 4 months in this location. Request a Private or Public Class

CT

For this location, please click here to visit our ATP site for the complete class schedule

DC

For this location, please click here to visit our ATP site for the complete class schedule

DE

For this location, please click here to visit our ATP site for the complete class schedule

FL

No classes scheduled for the next 4 months in this location. Request a Private or Public Class

GA

Advanced FPGA Implementation

Advanced FPGA Implementation tackles the most sophisticated aspects of the ISE® 10.1 design suite and Xilinx hardware. Seven labs provide hands-on experience in this two-day course and cover the Xilinx Synthesis Technology (XST) tools. This course requires the Fundamentals of FPGA Design and Designing for Performance courses as prerequisites. An intermediate knowledge of Verilog or VHDL is strongly recommended as is at least six months of design experience with Xilinx tools and FPGAs. The lecture material in this course covers the ISE 10.1 tools and the Virtex®-5 and Spartan®-3E FPGAs. For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
4/28/09 - 4/29/09	00058600	GA	Hyatt Place Atlanta/Duluth/Johns Creek	1400	14	Click here to Login & Register

Designing for Performance

Attending the Designing for Performance class will help you create more efficient designs. This course can help you fit your design into a smaller FPGA or a lower speed grade for reducing system costs. In addition, by mastering the tools and the design methodologies presented in this course, you will be able to create your design faster, shorten your development time, and lower development costs. For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
1/14/09 - 1/15/09	00058106	GA		1300	13	Click here to Login & Register

Embedded Systems Development

Xilinx FPGAs provide a new level of system design capabilities through soft MicroBlaze™ processors, hard PowerPC® processors, and silicon-efficient architectural resources. This course brings experienced FPGA designers up to speed on developing embedded systems using the Embedded Development Kit (EDK). The features and capabilities of the Xilinx MicroBlaze soft processor and the PowerPC 440 processor are also included in the lectures and labs. The hands-on labs provide experience with the development, debugging, and simulation of an embedded system. For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
3/17/09 - 3/18/09	00058592	GA	Hyatt Place Atlanta/Duluth/Johns Creek	1400	14	Click here to Login & Register

Fundamentals of FPGA Design

Use the ISE® software tools to implement a design and gain a firm understanding of the Xilinx FPGA architecture. Learn the best design practices from the pros and understand the subtleties of the Xilinx design flow. This course covers ISE 10.1 features, such as the Architecture Wizard and the Floorplan Editor. Other topics include design planning, implementation options, and global timing constraints. For more emphasis on improving the overall design performance take the follow-up course Designing for Performance, which builds on the basic principles covered in this course. Note that one of the prerequisites of Fundamentals of FPGA Design is the completion of the basic FPGA architecture modules. For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
1/13/09 - 1/13/09	00058105	GA		600	6	Click here to Login & Register

ID

No classes scheduled for the next 4 months in this location. Request a Private or Public Class

IL

No classes scheduled for the next 4 months in this location. Request a Private or Public Class

IN

No classes scheduled for the next 4 months in this location. Request a Private or Public Class

Internet (e-Learning)

No classes scheduled for the next 4 months in this location. Request a Private or Public Class

MA

Advanced Features and Techniques of Embedded Systems Development

Advanced Features and Techniques of Embedded Systems Development provides embedded systems developers the necessary skills to develop complex embedded systems and enables them to improve their designs by using the tools available in the Embedded Development Kit (EDK). This course also helps developers understand and utilize advance components of embedded systems design for architecting a complex system. For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
3/3/09 - 3/4/09	00058585	MA	Genesis Associates - Burlington, MA	1500	15	Click here to Login & Register

Advanced FPGA Implementation

Advanced FPGA Implementation tackles the most sophisticated aspects of the ISE® 10.1 design suite and Xilinx hardware. Seven labs provide hands-on experience in this two-day course and cover the Xilinx Synthesis Technology (XST) tools. This course requires the Fundamentals of FPGA Design and Designing for Performance courses as prerequisites. An intermediate knowledge of Verilog or VHDL is strongly recommended as is at least six months of design experience with Xilinx tools and FPGAs. The lecture material in this course covers the ISE 10.1 tools and the Virtex®-5 and Spartan®-3E FPGAs. For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
1/20/09 - 1/21/09	00058108	MA	Genesis Associates - Burlington, MA	1400	14	Click here to Login & Register

Designing with the PlanAhead Analysis and Design Tool

Learn to increase design performance and achieve repeatable results by using the PlanAhead software tool. Topics include: a product overview, synthesis and project tips, design analysis, creating a floorplan, improving performance, experimenting with implementation options, incremental methodology, block-based IP design, and I/O pin assignment.

Note: The hands-on labs provided within this course are identical to the tutorials that are packaged with the PlanAhead tool. This course is supplemented with instructor-led presentations and demos. For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
3/24/09 - 3/25/09	00058595	MA	Genesis Associates - Burlington, MA	1400	14	Click here to Login & Register

Embedded Open-Source Linux Development

This intermediate-level, two-day course provides embedded systems developers with experience in creating an embedded open-source Linux operating system on a Xilinx development board. The course offers students hands-on experience from building the environment to booting the system using a basic, single-processor System on Chip (SoC design with Linux 2.6 from the Xilinx kernel tree. This course introduces embedded Linux components, use of open-source components, environment configurations, network components, and debugging/profiling options for embedded Linux platforms. The primary focus is on embedded Linux development in conjunction with the Xilinx tool flo For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
1/7/09 - 1/8/09	00055743	MA	Genesis Associates - Burlington, MA	1600	16	Click here to Login & Register

Minimizing Your Design Time with the ChipScope Pro Debug and Verification Tools

Date	Class#	Location	Facility	Price (USD)	TC	
3/26/09 - 3/26/09	00058597	MA	Genesis Associates - Burlington, MA	600	6	Click here to Login & Register

For this location, please click here to visit our ATP site for the complete class schedule

MANITOBA

For this location, please click here to visit our ATP site for the complete class schedule

MD

For this location, please click here to visit our ATP site for the complete class schedule

ME

For this location, please click here to visit our ATP site for the complete class schedule

MI

No classes scheduled for the next 4 months in this location. Request a Private or Public Class

MN

No classes scheduled for the next 4 months in this location. Request a Private or Public Class

MO

No classes scheduled for the next 4 months in this location. Request a Private or Public Class

NC

No classes scheduled for the next 4 months in this location. Request a Private or Public Class

NH

For this location, please click here to visit our ATP site for the complete class schedule

NJ

For this location, please click here to visit our ATP site for the complete class schedule

NM

For this location, please click here to visit our ATP site for the complete class schedule

NORTEL

No classes scheduled for the next 4 months in this location. Request a Private or Public Class

NV

Advanced VHDL

Increase your VHDL proficiency by learning advanced techniques that help you write more robust and reusable code. This comprehensive course is targeted toward designers who already have some experience with VHDL. The course highlights modeling, testbenches, RTL/synthesizable design, and techniques aimed at creating parameterizable and reusable designs. For more information, see the course PDF, below.

Date	Class#	Location	Facility	Price (USD)	TC	
1/8/09 - 1/9/09	00057766	NV	Watermark Executive Suites - Las Vegas	1000	10	Click here to Login & Register

For this location, please click here to visit our ATP site for the complete class schedule

NY

For this location, please click here to visit our ATP site for the complete class schedule

OH

No classes scheduled for the next 4 months in this location. Request a Private or Public Class

ONTARIO

Advanced FPGA Implementation

Advanced FPGA Implementation tackles the most sophisticated aspects of the ISE® 10.1 design suite and Xilinx hardware. Seven labs provide hands-on experience in this two-day course and cover the Xilinx Synthesis Technology (XST) tools. This course requires the Fundamentals of FPGA Design and Designing for Performance courses as prerequisites. An intermediate knowledge of Verilog or VHDL is strongly recommended as is at least six months of design experience with Xilinx tools and FPGAs. The lecture material in this course covers the ISE 10.1 tools and the Virtex®-5 and Spartan®-3E FPGAs. For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
4/1/09 - 4/2/09	00058599	ONTARIO	Electro-Source Kanata (Ottawa) Ontario, CAN	1400	14	Click here to Login & Register

DSP Design Using System Generator

This course allows you to explore the System Generator tool and to gain the expertise you need to develop advanced, low-cost DSP designs. This intermediate course in implementing DSP functions focuses on learning how to use Syste Generator for DSP, design implementation tools, and hardware co-simulation verification. Through hands-on exercises you will implement a design from algorithm concept to hardware verification using the Xilinx FPGA capabilities. For mc information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
1/13/09 - 1/14/09	00055644	ONTARIO	Electro-Source Kanata (Ottawa) Ontario, CAN	1400	14	Click here to Login & Register

Embedded Systems Development

Xilinx FPGAs provide a new level of system design capabilities through soft MicroBlaze™ processors, hard PowerPC® processors, and silicon-efficient architectural resources. This course brings experienced FPGA designers up to speed on developing embedded systems using the Embedded Development Kit (EDK). The features and capabilities of the Xilinx MicroBlaze soft processor and the PowerPC 440 processor are also included in the lectures and labs. The hands-on labs provide experience with the development, debugging, and simulation of an embedded system. For mor information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
2/3/09 - 2/4/09	00058425	ONTARIO	Electro-Source - Rexdale (Toronto), CANADA	1400	14	Click here to Login & Register
2/9/09 - 2/10/09	00058427	ONTARIO	Electro-Source Kanata (Ottawa) Ontario, CAN	1400	14	Click here to Login & Register
2/10/09 - 2/11/09	00058426	ONTARIO	Electro-Source Kanata (Ottawa) Ontario, CAN	1400	14	Click here to Login & Register

Introduction to VHDL

This comprehensive course is a thorough introduction to the VHDL language. The emphasis is on writing Register Transfer Level (RTL) and behavioral source code. This class addresses targeting Xilinx devices specifically and FPGA devices in general. The information gained can be applied to any digital design by using a top-down synthesis design approach. You will also learn advanced coding techniques that will increase your overall VHDL proficiency and prepar you for the Advanced VHDL course.

Date	Class#	Location	Facility	Price (USD)	TC	
2/24/09 - 2/26/09	00058429	ONTARIO	Electro-Source - Rexdale (Toronto), CANADA	1800	18	Click here to Login & Register

For this location, please click here to visit our ATP site for the complete class schedule

OR

No classes scheduled for the next 4 months in this location. Request a Private or Public Class

PA - EASTERN

For this location, please click here to visit our ATP site for the complete class schedule

PA - WESTERN

No classes scheduled for the next 4 months in this location. Request a Private or Public Class

QUEBEC

Advanced FPGA Implementation

Advanced FPGA Implementation tackles the most sophisticated aspects of the ISE® 10.1 design suite and Xilinx hardware. Seven labs provide hands-on experience in this two-day course and cover the Xilinx Synthesis Technology (XST) tools. This course requires the Fundamentals of FPGA Design and Designing for Performance courses as prerequisites. An intermediate knowledge of Verilog or VHDL is strongly recommended as is at least six months of design experience with Xilinx tools and FPGAs. The lecture material in this course covers the ISE 10.1 tools and the Virtex®-5 and Spartan®-3E FPGAs. For more information, see the detailed course description.

Date	Class#	Location	Facility	Price (USD)	TC	
1/27/09 - 1/28/09	00058125	QUEBEC	Hardent - Montreal, Quebec, CAN	1400	13	Click here to Login & Register

For this location, please click here to visit our ATP site for the complete class schedule

RI

For this location, please click here to visit our ATP site for the complete class schedule

SASKATCHEWAN

For this location, please click here to visit our ATP site for the complete class schedule

TN

No classes scheduled for the next 4 months in this location. Request a Private or Public Class

TX

No classes scheduled for the next 4 months in this location. Request a Private or Public Class

UT

No classes scheduled for the next 4 months in this location. Request a Private or Public Class

VA

For this location, please click here to visit our ATP site for the complete class schedule

VT

For this location, please click here to visit our ATP site for the complete class schedule

WA

No classes scheduled for the next 4 months in this location. Request a Private or Public Class

WI

No classes scheduled for the next 4 months in this location. Request a Private or Public Class