

Overview

The purpose of this notification is to communicate the addition of new shipping trays for the VO48/VOG48 TSOP packages.

Description

Xilinx is adding new shipping trays produced by PCT Technology for the VO48/VOG48 TSOP packages.

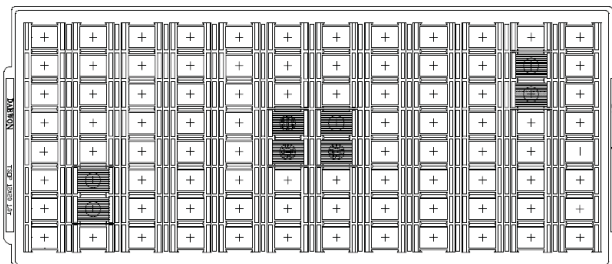
The new PCT Technology tray matrices are designed to be compatible with current Daewon shipping trays. The PCT Technology trays are stackable with Daewon trays, and will have the same X/Y tray dimensions as the Daewon trays.

Xilinx will begin using the new shipping product in the PCT Technology trays starting May 1, 2008.

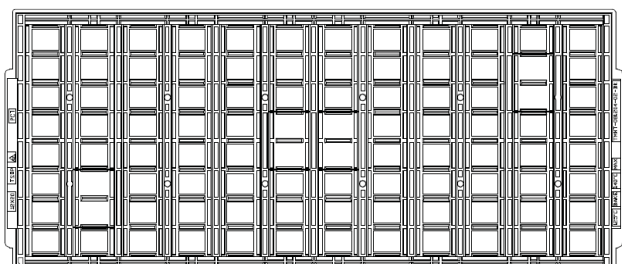
Table 1 and Figure 1 provide further details of the PCT Technology shipping tray.

Table 1: PCT Technology Tray Details

Tray Description	Current Tray		Additional Tray	
	Daewon Tray Part Number	Xilinx Drawing	PCT Technology Tray Part Number	Xilinx Drawing
12X20 TSOP Thin Matrix Tray (96 pockets) for VO48/VOG48 package	124-1220-113B	SIT0102	TMHT-081204-02-BK	SIT0122



Tray 1: 124-1220-113B
Vendor: Daewon



Tray 2: TMHT-081204-02-B
Vendor: PCT Technology

XCN07020_01_030608

Figure 1: Tray Details

Products Affected

This notification applies only to the following Xilinx Commercial and Military part numbers and all associated SCD part numbers (see [Table 2](#)).

Table 2: Affected Devices

New VO48/VOG48 Shipping Trays		
Device	Device	Device
XCF08PVO48C	XCF16PVO48C	XCF32PVO48C
XCF08PVOG48C	XCF16PVOG48C	XCF32PVOG48C
		XQF32PVO48M

Response

Note: In accordance with JESD46-C, this change is deemed accepted by the customer if no acknowledgement is received within 30 days from this notice.

No response is required by this notice. For additional information or questions, please contact [Xilinx Technical Support](#).

Important Notice: Xilinx Customer Notifications (XCNs, XDNs, and Quality Alerts) can be delivered via e-mail alerts sent by the MySupport website (<http://www.xilinx.com/support>). Register today and personalize your “MyAlerts” area to include Customer Notifications. This change provides many benefits, including the ability to receive alerts for new and updated information about specific products, as well as alerts for other publications such as data sheets, errata, application notes, etc. For information on how to sign up, refer to Xilinx [Answer Record 18683](#).

Additional Documentation

None available.

Revision History

The following table shows the revision history for this document.

Date	Version	Revision
03/24/08	1.0	Initial release.
3/28/08	1.0.1	Corrected Daewon Tray Part Number and Xilinx Drawing in Table 1 .
5/23/08	1.0.2	Minor editorial update; added Additional Documentation section.